T.E. sem VI (ETRX) (CB QS) DSP&P

Q.P. Code : 6432

(3 Hours)

[Total Marks : 80

14/12/15

5 5

10

N.B.: (1) Question No.1 is compulsory.

- (2) Answer any three questions from remaining five question.
- (3) All questions carry equal marks.
- 1. (a) Justify: In impulse invariance transformation method there is many to one mapping of poles from s-plane to z-plane.
 - (b) Find the number of computations required to compute 32 point DFT using direct calculation and by using FFT algorithm. Also find the computational complexity.
 - (c) Compare DSP processor and microprocessor.
 - (d) Compare fixed point arithmetic and floating point arithmetic.
- 2. (a) Find the DFT of the following sequence using Radix 2 DIF FFT algorithm 10 $x(n) = \{1, 2, 3, 4, 4, 3, 2, 1\}$
 - (b) Compute the circular convolution of the sequences using DFT and IDFT 10 approach.

 $x_{1}(n) \{1, 2, 0\}$ $x_{n}(n) = \{2, 2, 1, 1\}$

3. (a) Design a Low pass FIR filter with 11 coefficients for the following 10 specifications. Passband frequency edge = 0.25KHz and sampling frequency = 1 KHz

Use rectangular window in the design.

- (b) Explain frequency sampling method of designing FIR filter.
- 4. (a) Use bilinear transformation to obtain a digital filter of notch frequency 10 75Hz and sampling frequency of 200 Hz, for a given normalized second

order filter having transfer function $H(S) = \frac{S^2 + 1}{S^2 + S + 1}$

(b) Design a Butterworth lowpass filter to meet the following specifications. 10
 Passband gain = 0.89
 Passband frequency edge = 30Hz
 Attenuation = 0.20
 Stopband edge = 75Hz

SPECIFIC MD-Con. 11284-15.

Q.P. Code : 6432

2

PATELINSTITUTE OF TECHNOLOG SSC 5. (a) Explain with neat diagram architecture of TMS320C67XX DSP processor.

(b) Explain the applications of the DSP processor in following fields.

- Radar signal processing (i)
- Speech recognition. (ii)

6. (a) Draw the quantization noise model for second order system.

$$H(z) = \frac{1}{1 - 2r\cos\theta z^{-1} + r^2 z^{-2}}$$

find the steady state output noise variance.

(b) Explain the following terms.

(i) Dead band

- Limit cycle oscillations (ii)
- (iii) Addressing modes of TMS320C67XX processor. PRACE PARTING OF THE MAN PARTICIPATION OF THE MAN PARTICIPATION OF THE MAN PARTICIPATION OF THE PARTICIPATION OF T